



**Office Action Summary**

Application No.	Applicant(s)
10/082,984	KONISHI ET AL.
Examiner	Art Unit
George Y. Wang	2871

*-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --*

**Period for Reply****A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.**

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).
- Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

**Status**

1)  Responsive to communication(s) filed on \_\_\_\_\_.

2a)  This action is FINAL.

2b)  This action is non-final.

3)  Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

**Disposition of Claims**

4)  Claim(s) 1-6 is/are pending in the application.

4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5)  Claim(s) \_\_\_\_\_ is/are allowed.

6)  Claim(s) 1-6 is/are rejected.

7)  Claim(s) \_\_\_\_\_ is/are objected to.

8)  Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

**Application Papers**

9)  The specification is objected to by the Examiner.

10)  The drawing(s) filed on 25 February 2002 is/are: a)  accepted or b)  objected to by the Examiner.  
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

11)  The proposed drawing correction filed on \_\_\_\_\_ is: a)  approved b)  disapproved by the Examiner.  
If approved, corrected drawings are required in reply to this Office action.

12)  The oath or declaration is objected to by the Examiner.

**Priority under 35 U.S.C. §§ 119 and 120**

13)  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a)  All b)  Some \* c)  None of:

1.  Certified copies of the priority documents have been received.

2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.

3.  Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

\* See the attached detailed Office action for a list of the certified copies not received.

14)  Acknowledgment is made of a claim for domestic priority under 35 U.S.C. § 119(e) (to a provisional application).

a)  The translation of the foreign language provisional application has been received.

15)  Acknowledgment is made of a claim for domestic priority under 35 U.S.C. §§ 120 and/or 121.

**Attachment(s)**

1)  Notice of References Cited (PTO-892)  
2)  Notice of Draftsperson's Patent Drawing Review (PTO-948)  
3)  Information Disclosure Statement(s) (PTO-1449) Paper No(s) \_\_\_\_\_.

4)  Interview Summary (PTO-413) Paper No(s) \_\_\_\_\_.  
5)  Notice of Informal Patent Application (PTO-152)  
6)  Other: \_\_\_\_\_

**DETAILED ACTION**

***Drawings***

1. Figures 4-8 should be designated by a legend such as --Prior Art-- because only that which is old is illustrated. See MPEP § 608.02(g). A proposed drawing correction or corrected drawings are required in reply to the Office action to avoid abandonment of the application. The objection to the drawings will not be held in abeyance.

***Specification***

2. The title of the invention is not descriptive. A new title is required that is clearly indicative of the invention to which the claims are directed.

***Claim Rejections - 35 USC § 103***

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was

not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

4. Claims 1-6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Fujihara et al (U.S. Patent No. 5,771,083, from hereinafter "Fujihara") in view of Ellis (U.S. Patent No. 5,546,204).

5. As to claim 1, Fujihara discloses a liquid crystal display (LCD) having a TFT substrate (fig. 1, ref. 8) and a counter substrate (fig. 2, ref. 1) with liquid crystal layer (col. 1, lines 41-43), where the TFT array substrate has a display area and a terminal forming area such that the display area has a pixel electrode (fig. 1, ref. 5), a switching element (fig. 1, ref. 8), a gate line (fig. 1, ref. 2) and a source line (fig. 1, ref. 3) while the terminal forming area has a terminal electrode for connecting to the gate or source line to an external signal source. Furthermore, the first metallic line and the second metallic line are both connected to the terminal electrode via respective contact holes (fig. 2, ref. 13) arranged below the terminal electrode, and an insulating layer (fig. 2, ref. 9) is interposed between the first metallic line and the second metallic line.

However, the reference fails to specifically disclose a liquid crystal layer interposed between the TFT substrate and counter substrate.

It would have been obvious to one of ordinary skill in the art at the time the invention was made to have positioned the liquid crystal layer between the two



Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-308-0956.

*GW*  
gw  
May 16, 2003

*SEARCHED* *INDEXED* *MAILED*  
TELEPHONIC COMMUNICATIONS  
SEARCHED INDEXED MAILED  
TELEPHONIC COMMUNICATIONS